

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings of claims in the application:

**Listing of Claims:**

1                   1.       (original) A method of transferring signals from a plurality of individual  
2 sensing elements provided on a first integrated circuit to a processing means provided on a  
3 second integrated circuit comprising the steps of sequentially sampling the output of a number of  
4 sensing elements in a predetermined sequence to create a first signal, modulating the amplitude  
5 of a constant frequency signal to create a second signal, transmitting said second signal from said  
6 first integrated circuit to said second integrated circuit, demodulating said second signal to  
7 regenerate said first signal and passing said regenerated first signal to said processing means.

1                   2.       (original) A method according to Claim 1 wherein the outputs of a first  
2 group of individual sensing elements are sampled and are then used to modulate a carrier signal  
3 of constant known frequency and, the output of a second group of individual sensing elements is  
4 simultaneously sampled and used to modulate a carrier signal of a different constant known  
5 frequency, both modulated signals being simultaneously transmitted to said second integrated  
6 circuit and simultaneously demodulated after arriving at said second integrated circuit.

1                   3.       (original) A method according to Claim 2 wherein outputs of several such  
2 groups of individual sensing elements are simultaneously sampled, modulated, transmitted and  
3 demodulated.

1                   4.       (original) A method according to Claim 3 wherein the groups of sensing  
2 elements correspond to individual rows or columns in a sensing array, the sampling sequence  
3 within the group starting with a sensor at one end of the said row or column and finishing with  
4 the sensor at the opposite end of said row or column.

1                   5.       (original) A method according to Claim 4 wherein each row or column is  
2 provided with dedicated modulating means and the modulated signals are subsequently  
3 combined by a suitable combining means.

1                   6.       (currently amended) A method according to ~~any preceding claim~~ 1  
2 wherein the sampling process is repeated instantly.

1                   7.       (currently amended) A method according to ~~any preceding~~ Claim 1  
2 wherein there is a predetermined delay between the successive sampling sequences.

1                   8.       (currently amended) A method according to ~~any preceding claim~~ 1  
2 wherein said second signal undergoes analogue to digital conversion and is subsequently  
3 demodulated as a digital process.

1                   9.       (original) A method according to Claim 8 wherein signals resulting from  
2 a digital demodulation process are stored in registers for further image processing.

1                   10.     (currently amended) A method according to ~~any one of Claims 4 to 9~~  
2 claim 4 wherein each carrier signal for each row or column or group of the individual sensing  
3 elements has a different frequency.

1                   11.     (original) A method according to Claim 10 wherein carrier frequencies  
2 are determined such that any odd harmonics that may be generated during the modulation  
3 process using one carrier frequency are at frequencies that do not fall close to other carrier  
4 frequencies.

1                   12.     (original) A method according to Claim 11 wherein carrier frequencies  
2 are produced by integer division from a single clock frequency signal.

1                   13.     (original) A method according to Claim 12 wherein a suitable clock  
2 frequency is 1 Megahertz and a suitable integer division ratios are one of 18, 20, 22, 25, 28, 33,  
3 40 and 50.

1                   14.   (original) A sensing device comprising an array of individual sensing  
2 elements provided on a first integrated circuit and processing means for the output of said array  
3 of said sensing elements provided on a second integrated circuit, said circuits being linked by a  
4 single conducting connection, said first integrated circuit comprising in addition to said sensing  
5 elements, sampling means for sequentially sampling the output of said sensing elements in a  
6 predetermined order to generate a first signal, signal generator means for generating a carrier  
7 signal of a constant known frequency, modulation means for modulating said carrier signal with  
8 said first signal to generate a second signal, and transmission means for transmitting said second  
9 signal to the second integrated circuit, said second integrated circuit incorporating means for  
10 receiving said second signal, means for demodulating said second signal to regenerate said first  
11 signal and means for processing said regenerated first signal.

1                   15.   (original) A sensing device according to Claim 14 wherein the outputs of  
2 a first group of individual sensing elements are sampled and are then used to modulate a carrier  
3 signal of constant known frequency and, the output of a second group of individual sensing  
4 elements is simultaneously sampled and used to modulate a carrier signal of a different constant  
5 known frequency, both modulated signals being simultaneously transmitted to said second  
6 integrated circuit and simultaneously demodulated after arriving at said second integrated circuit.

1                   16.   (currently amended) A sensing device according to ~~Claim 14~~ or Claim 15  
2 wherein outputs of several such groups of individual sensing elements are simultaneously  
3 sampled, modulated, transmitted and demodulated.

1                   17.   (original) A sensing device according to Claim 16 wherein the groups of  
2 sensing elements correspond to individual rows or columns in the sensing array, the sampling  
3 sequence within the group starting with the sensor at one end of the said row or column and  
4 finishing with the sensor at the opposite end of said row or column.

1                    18.    (original) A sensing device according to Claim 17 wherein each row or  
2 column is provided with dedicated modulating means and the modulated signals are  
3 subsequently combined by a suitable combining means.

1                    19.    (currently amended) A sensing device according to ~~any one of Claims 14~~  
2 ~~to 18~~ claim 14 wherein the sampling process is repeated instantly.

1                    20.    (currently amended) A sensing device according to ~~any one of Claims 14~~  
2 ~~to 18~~ claim 14 wherein there is a predetermined delay between the successive sampling  
3 sequences.

1                    21.    (currently amended) A sensing device according to ~~any one of Claims 17~~  
2 ~~to 20~~ claim 17 wherein each sensing element in a row or column is connected to a row or column  
3 output conductor by a switch.

1                    22.    (currently amended) A sensing device according to ~~any one of Claims 14~~  
2 ~~to 21~~ claim 21 wherein sequential sampling of the outputs of individual sensing elements is  
3 carried out by sequentially connecting each sensing element to the row or column output  
4 conductor by closing each switch in turn.

1                    23.    (currently amended) A sensing device according to ~~Claim 21 or Claim 22~~  
2 in which each sensor generates a differential output, each said sensing element being connected  
3 by a pair of switches to a pair of output conductors, these switches being closed in turn.

1                    24.    (currently amended) A sensing device according to ~~any one of Claims 14~~  
2 ~~to 23~~ claim 14 wherein said second signal undergoes analogue to digital conversion and is  
3 subsequently demodulated as a digital process.

1                    25.    (original) A sensing device according to Claim 24 wherein signals  
2 resulting from a digital demodulation process are stored in registers for further image processing.

1                   26.   (currently amended) A sensing device according to Claim 24 ~~or Claim 25~~  
2   in which digital processing is carried out by a microprocessor.

1                   27.   (currently amended) A sensing device according to ~~any one of Claims 17~~  
2   ~~to 26~~ claim 17 wherein each carrier signal for each row or column or group of the individual  
3   sensing elements has a different frequency.

1                   28.   (original) A sensing device according to Claim 27 wherein carrier  
2   frequencies are determined such that any odd harmonics that may be generated during the  
3   modulation process using one carrier frequency are at frequencies that do not fall close to other  
4   carrier frequencies.

1                   29.   (currently amended) A sensing device according to Claim ~~29~~ 28 wherein  
2   carrier frequencies are produced by integer division from a single clock frequency signal.

1                   30.   (original) A sensing device according to Claim 29 wherein a suitable  
2   clock frequency is 1 Megahertz and a suitable integer division ratios are one of 18, 20, 22, 25,  
3   28, 33, 40 and 50.

1                   31.   (currently amended) A sensing device according to ~~any one of claims~~  
2   claim ~~26 to 31~~ wherein a ~~signal~~ clock signal generator or synchronisation signal generator is  
3   connected to both integrated circuits.

1                   32.   (currently amended) A sensing device according to ~~any preceding claim~~  
2   14 in which said sensing elements are IR (infrared) sensing elements.